L Number	Hits	Search Text	DB	Time stamp
1	6	(("5123555") or ("5444298") or ("5556811")).PN.	USPAT;	2003/11/28 11:09
			EPO; JPO;	
			DERWENT;	
			USOCR	
2	881	(257/691).CCLS.	USPAT:	2003/11/28 11:13
			EPO; JPO;	
			DERWENT;	
			USOCR	
3	133366	printed adj circuit adj board	USPAT;	2003/11/28 11:16
		•	EPO; JPO;	
			DERWENT;	
			USOCR	
4	62380	(printed adj circuit adj board) and (IC aj device)	USPAT;	2003/11/28 12:02
		a constant and the sign and the sign actions,	EPO; JPO;	2000/11/20 12.02
			DERWENT;	
			USOCR	
5	19330	((printed adj circuit adj board) and (IC aj device)) and	USPAT;	2003/11/28 11:17
		voltage	1	2003/11/28 11:17
		1190	EPO; JPO; DERWENT;	
6	29048	((printed adj circuit adj board) and (IC aj device)) and	USOCR	00004440044
~	20010	(pins or terminal)	USPAT;	2003/11/28 11:17
		(pino or terminary	EPO; JPO;	
			DERWENT;	
7	12508	(((printed adjairouit adj based) and ((C aj davisa)) and	USOCR	
'	12300	(((printed adj circuit adj board) and (IC aj device)) and (pins or terminal)) and voltage	USPAT;	2003/11/28 11:18
		(pins of terminar)) and voltage	EPO; JPO;	
			DERWENT;	
0	12508		USOCR	
8	12508	((((printed adj circuit adj board) and (IC aj device)) and	USPAT;	2003/11/28 12:02
		(pins or terminal)) and voltage) and (voltage reducer adj	EPO; JPO;	
ŀ	i	circuit)	DERWENT;	
0	40000		USOCR	
9	10236	(((((printed adj circuit adj board) and (IC aj device)) and	USPAT;	2003/11/28 11:19
		(pins or terminal)) and voltage) and (voltage reducer adj	EPO; JPO;	
	yo I	circuit)) and output	DERWENT;	
10	7200		USOCR	
10	7306	((((((printed adj circuit adj board) and (IC aj device)) and	USPAT;	2003/11/28 11:20
		(pins or terminal)) and voltage) and (voltage reducer adj	EPO; JPO;	
		circuit)) and output) and lower	DERWENT;	
	200		USOCR	
11	920	((((((printed adj circuit adj board) and (IC aj device)) and	USPAT;	2003/11/28 11:20
		(pins or terminal)) and voltage) and (voltage reducer adj	EPO; JPO;	
		circuit)) and output) and lower) and (voltage adj supply)	DERWENT;	
			USOCR	
12	656	(((((((printed adj circuit adj board) and (IC aj device))	USPAT;	2003/11/28 11:29
		and (pins or terminal)) and voltage) and (voltage reducer	EPO; JPO;	
		adj circuit)) and output) and lower) and (voltage adj	DERWENT;	
		supply)) and capacitor	USOCR	
13	395	(361/764).CCLS.	USPAT;	2003/11/28 11:48
			EPO; JPO;	
			DERWENT;	
			USOCR	
14	5547	361/\$.ccls. and (supply adj voltage)	USPAT;	2003/11/28 12:20
		, , , , , , , , , , , , , , , , , , , ,	EPO; JPO;	2000/11/20 12.20
			DERWENT;	
	1		USOCR	
15	404	(361/\$.ccls. and (supply adj voltag)) and (print d adj	USPAT;	2003/11/20 44:50
		circuit adj board)	EPO; JPO;	2003/11/28 11:50
	ĺ	- ··- · · · · · · · · · · · · · · · · ·		
	1		DERWENT;	
		1	HEALD	
16	143	((361/\$ ccls, and (supply adj voltage)) and (printed ad)	USOCR	0000144100 ** **
16	143	((361/\$.ccls. and (supply adj voltage)) and (printed adj	USPAT;	2003/11/28 11:50
16	143	((361/\$.ccls. and (supply adj voltage)) and (printed adj circuit adj board)) and IC		2003/11/28 11:50

17	66259	(voltage reducer adj circuit) same regulator	LICDAT	0000/44/00 40 00
• •	00233	(Voltage reducer adj circuit) Same regulator	USPAT;	2003/11/28 12:02
			EPO; JPO;	
			DERWENT;	
18	613	(IC adj device) same terminals	USOCR	
10	013	(To adj device) same terminals	USPAT;	2003/11/28 12:03
			EPO; JPO;	
			DERWENT;	
20	0	(((IC adi davias) sama tarminale) and (() altare and a	USOCR	
20	•	(((IC adj device) same terminals) and ((voltage reducer adj circuit) same regulator)) and (printed adj circuit adj	USPAT;	2003/11/28 12:03
		board)	EPO; JPO;	
		board)	DERWENT;	
19	18	(//C adi davias) some torreinale) and //watte	USOCR	
13	10	((IC adj device) same terminals) and ((voltage reducer	USPAT;	2003/11/28 12:05
		adj circuit) same regulator)	EPO; JPO;	
			DERWENT;	
21	1398	(264/760) 001.6	USOCR	
	1396	(361/760).CCLS.	USPAT;	2003/11/28 12:16
			EPO; JPO;	
			DERWENT;	
22	40	207/504	USOCR	
22	19	327/564.ccls. and (printed adj circuit adj board)	USPAT;	2003/11/28 12:18
			EPO; JPO;	
			DERWENT;	
23	50	007/504 500	USOCR	İ
23	59	327/564-566.ccls. and (printed adj circuit adj board)	USPAT;	2003/11/28 12:19
			EPO; JPO;	
			DERWENT;	
0.4	004		USOCR	
24	861	365\$.ccls. and (printed adj circuit adj board)	USPAT;	2003/11/28 12:20
			EPO; JPO;	
			DERWENT;	
05	-	0.0514	USOCR	
25	861	365/\$.ccls. and (printed adj circuit adj board)	USPAT;	2003/11/28 12:20
			EPO; JPO;	
			DERWENT;	
00		(00m)	USOCR	
26	201	(365/\$.ccls. and (printed adj circuit adj board)) and	USPAT;	2003/11/28 12:20
		(supply adj voltage)	EPO; JPO;	
			DERWENT;	
			USOCR	